

Electronic Acknowledgement Receipt

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Application Number:	10807286
International Application Number:	
Confirmation Number:	3924
Title of Invention:	SEMICONDUCTOR INTEGRATED CIRCUIT TIMING ANALYSIS APPARATUS TIMING ANALYSIS METHOD AND TIMING ANALYSIS PROGRAM
First Named Inventor/Applicant Name:	Tetsuo Kawano
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Attorney Docket Number:	1450.1040
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Application Type:	Utility

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Submitted with Payment	yes
Payment was successfully received in RAM	\$ 1700
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
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1	Issue Fee Payment (PTO-85B)	IssueandPubFees-4062007.pdf	104305	no	1
Warnings:					
Information:					
2	Fee Worksheet (PTO-06)	fee-info.pdf	8331	no	2
Warnings:					
Information:					
Total Files Size (in bytes):			112636		
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